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| International Application Number: | | | | | |
| Confirmation Number: | 8919 | | | | |
| Title of Invention: | Method and apparatus for test and characterization of semiconductor components | | | | |
| First Named Inventor/Applicant Name: | Frederick Ware | | | | |
| Correspondence Address: | Kirk J. DeNiro, Esq. Vierra Magen Marcus Harmon & DeNiro LLP 685 Market Street Suite 540 San Francisco CA 94105 US 415-369-9665 | | | | |
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File Listing:

| Document Number | Document Description | File Name | File Size(Bytes) | Multi Part /.zip | Pages (if appl.) | | | | |
|--------------------|---|-----------------|------------------|---------------------|---------------------|--|--|--|--|
| 1 | Request for Continued Examination (RCE) | 1033us1-rce.pdf | 666568 | no | 3 | | | | |
| Warnings: | | | | | | | | | |
| Information: | | | | | | | | | |
| 2 | Extension of Time | 1033us1-ext.pdf | 145658 | no | 1 | | | | |
| Warnings: | | | | | | | | | |
| Information: | | | | | | | | | |
| 3 | Fee Worksheet (PTO-06) | fee-info.pdf | 8345 | no | 2 | | | | |
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